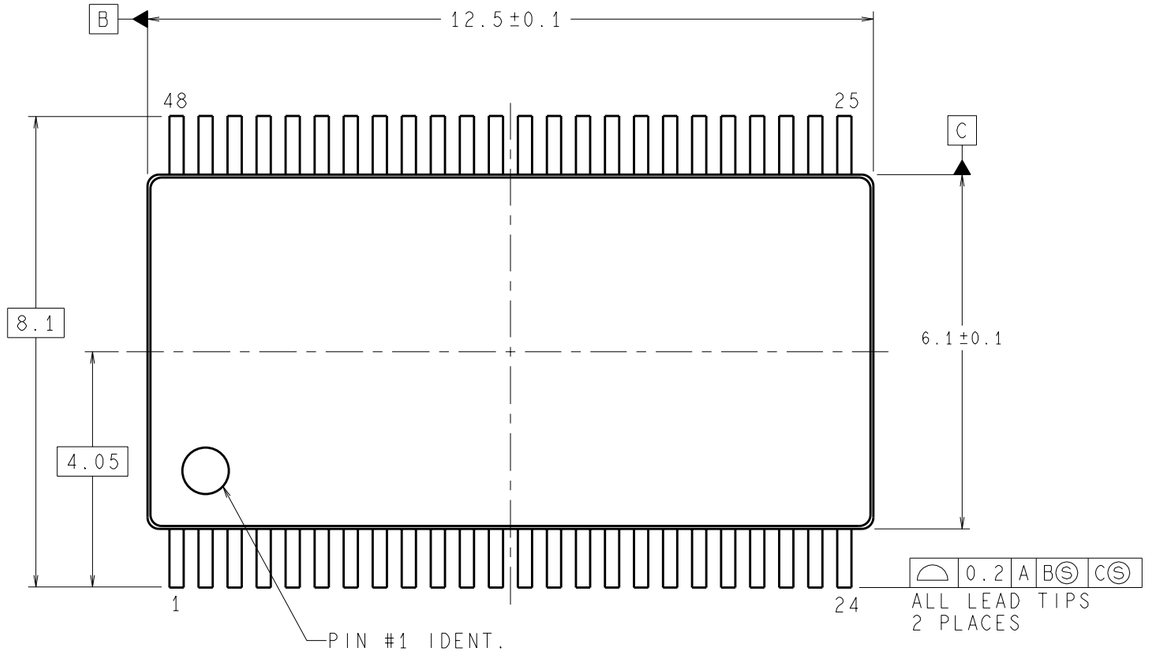
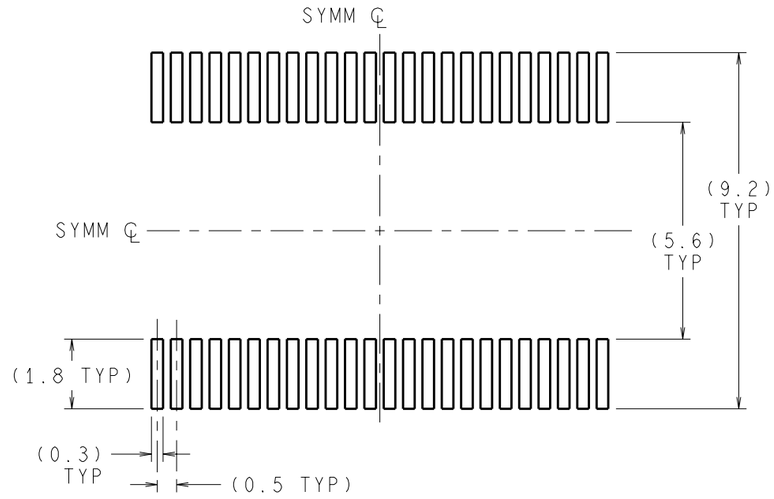
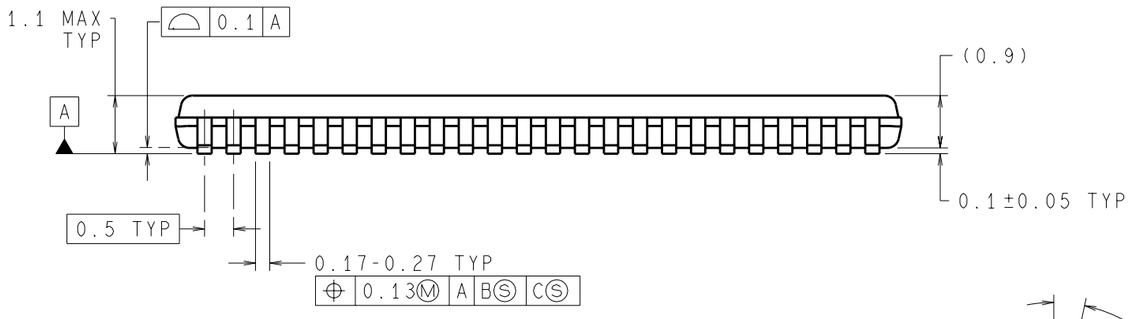
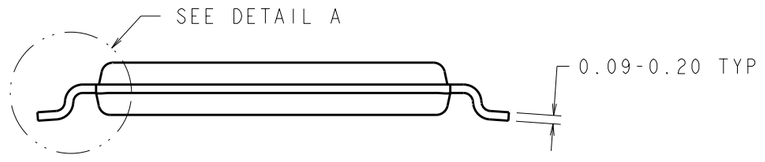
REVISIONS				
LTR	DESCRIPTION	E.C.N.	DATE	BY/APP'D
B	REDRAW ON PRO/E, ADD LAND PATTERN RECOMENDATION.	11322	02/16/1996	MS/CS
C	DIM 6.1±0.1 WAS 6.1±0.01; REVISE NOTE 1.	197	12/06/2000	MS/CD



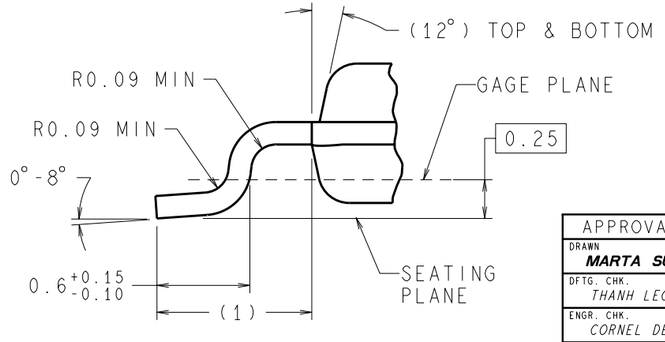


LAND PATTERN RECOMENDATION





DIMENSIONS ARE IN MILLIMETERS



DETAIL A
TYPICAL, SCALE: 40X

NOTES: UNLESS OTHERWISE SPECIFIED

1. REFERENCE JEDEC REGISTRATION MO-153, VARIATION ED.

APPROVALS	DATE
DRAWN *MARTA SUCHY*	02/16/1996
DFTG. CHK. *THANH LEQUANG*	12/06/2000
ENGR. CHK. *CORNEL DEVERA*	12/06/2000

PROJECTION
INCH
[MM]

National Semiconductor
2900 Semiconductor Dr, Santa Clara, CA 95052-8090

MOLDED TSSOP, JEDEC, 6.1mm BODY WIDTH, 48 LD, 0.50mm PITCH

SCALE	SIZE	DRAWING NUMBER	REV
N/A	C	(SC)MKT-MTD48	C

DO NOT SCALE DRAWING | SHEET 1 of 1